### 3.2 Modular Exponentiation

In order to compute the modular exponentiation $\mathrm{M}^{\mathrm{e}} \boldsymbol{\operatorname { m o d }} \mathrm{N}$ for cryptography applications, the exponentiation $\mathrm{M}^{\mathrm{e}}$ is not computed first, rather a part of the exponentiation operation, usually a partial multiplication, is performed. Then the partial result is reduced modulo N at each step. This is simply because the exponentiation result of large numbers is very large. As an instance, the result of $\mathrm{M}^{\mathrm{e}}$ when M and e are 256-bit integers, requires about $2^{256}$ bits which is larger than the amount of total articles in the universe. Similar to the exponentiation procedure which performs a successive multiplications, modular exponentiation uses the same procedure as exponentiation, but it performs modular multiplication. Therefore the exponentiation methods can be used for modular exponentiation without loss of generality. The next section briefly describes the exponentiation algorithms, further details of which can be found in the appropriate references.

### 3.2.1 Binary Methods

The two commonly used algorithms which are for hardware implementation of exponentiation are the left-to-right and right-to-left algorithms. These algorithms, which are also called the square and multiply method, perform exponentiation by repeated squaring and multiplication.

Left-to-Right algorithm: For the integers $M, N, C$ and $e=\left\{\mathrm{e}_{\mathrm{n}-1} \mathrm{e}_{\mathrm{n}-2} \ldots \mathrm{e}_{0}\right\}, \mathrm{e}_{\mathrm{i}} \in\{0,1\}$, the following algorithm computes $\mathrm{C}=\mathrm{M}^{\mathrm{e}} \bmod \mathrm{N}$ :

## The Left-to-Right Exponentiation Algorithm

```
C := 1;
for i = n-1 downto 0 loop
    C := C}\mp@subsup{}{}{2}\operatorname{mod}N\mathrm{ ;
    if ei = 1 then C := M.C mod N;
return C;
```

where $\mathrm{C}=\mathrm{M}^{\mathrm{e}} \bmod \mathrm{N}$. According to Knuth [Knu98], this method was known as early as 200 B.C. by Indian mathematicians. However, a clear discussion of how to compute $2^{n}$ efficiently for arbitrary n was given by al-Uqlidisi, an Islamic mathematician, in 952 A.D. Beginning initially with $\mathrm{C}=1$, this algorithm sets C to $\mathrm{C}^{2}$ and then, if $\mathrm{e}_{\mathrm{i}}=1$, sets C to $\mathrm{C} . \mathrm{M}$. The exponent bits are scanned from left to right and the zeros before the MSB are ignored. The following binary representation of the exponent e can be used for validity of this algorithm as:

$$
\mathrm{e}=\sum_{i=0}^{n-1} \mathrm{e}_{\mathrm{i}} \cdot 2^{\mathrm{i}}=\left(\left(\ldots\left(\left(\mathrm{e}_{\mathrm{n}-1}\right) \cdot 2+\mathrm{e}_{\mathrm{n}-2}\right) \cdot 2+\ldots\right) \cdot 2+\mathrm{e}_{1}\right) \cdot 2+\mathrm{e}_{0}
$$

Therefore the exponentiation can be expressed as:

$$
\mathbf{M}^{\mathrm{e}}=\mathrm{M}^{\left.\left.\left(\left(\ldots . .\left(\mathrm{e}_{\mathrm{n}-1}\right) \cdot 2+\mathrm{e}_{\mathrm{n}-2}\right) \cdot 2 \ldots\right) \cdot 2+\mathrm{e}_{1}\right) \cdot 2+\mathrm{e}_{0}\right)}=\left(\left(\ldots\left(\left(\mathrm{M}_{\mathrm{n}-1} \mathrm{e}^{2}\right)^{2}\right)^{2} \cdot \mathrm{M}_{\mathrm{n}-2} \mathrm{e}^{\mathrm{e}} \cdot\right)^{2} \cdot \mathrm{M}_{1}{ }^{\mathrm{e}}\right)^{2} \cdot \mathrm{M}^{\mathrm{e}_{0}}
$$

For example:

$$
\mathrm{M}^{23}=\mathrm{M}^{(10111) \mathrm{b}}=\left(\left(\left(\left((1)^{2} \cdot \mathrm{M}\right)^{2}\right)^{2} \cdot \mathrm{M}\right)^{2} \cdot \mathrm{M}\right)^{2} \cdot \mathrm{M}
$$

The left-to-right algorithm involves $\left\lfloor\log _{2}{ }^{e}\right\rfloor$ squarings and $v(n)-1$ multiplications (or $\left\lfloor\log _{2}{ }^{\mathrm{e}}\right\rfloor+$ $v(\mathrm{n})-1$ multiplications if both operands use the multiplier) when both operations on the initial 1 are ignored. $v(n)$ is the number of non-zero bits in the binary representation of e . In a hardware implementation, this algorithm requires one storage register to keep the intermediate result.

The right-to-Left algorithm: for i from 0 up to $\mathrm{k}-1$, this algorithm first sets C to $\mathrm{C} . \mathrm{M}$ if $\mathrm{e}_{\mathrm{i}}=1$ and then sets M to $\mathrm{M}^{2}$.

The algorithm can be also written for modular exponentiation as follows :

## The Right-to-Left Exponentiation Algorithm

```
C := 1;
for i = n-1 downto 0 loop
        IF e}\mp@subsup{e}{i}{}=1\mathrm{ THEN C := M.C mod N;
        M := M }\mp@subsup{}{}{2}\operatorname{mod}N
return C;
```

where $\mathrm{C}=\mathrm{M}^{\mathrm{e}} \mathbf{\operatorname { m o d }} \mathrm{N}$. According to Knuth [Knu98], al-Kashi an Iranian mathematician stated this algorithm about 1414 A.D. The method is closely related to the multiplication procedure used by Egyptian mathematicians as early as 1800 B.C and widely used in Russia in the nineteenth century. It is often called "Russian peasant method" of multiplication.

This method is easily justified by a consideration of the sequence of exponents in the calculation. Since:

$$
\mathrm{e}=\sum_{i=0}^{n-1} \mathrm{e}_{\mathrm{i}} \cdot 2^{\mathrm{i}}=2^{\mathrm{n}-1} \cdot \mathrm{e}_{\mathrm{n}-1}+2^{\mathrm{n}-1} \cdot \mathrm{e}_{\mathrm{n}-1}+\ldots+2^{1} \cdot \mathrm{e}_{1}+2^{0} \cdot \mathrm{e}_{0}
$$

The exponentiation can be written as:

$$
\begin{aligned}
& \mathrm{M}^{\mathrm{e}}=(\mathrm{M})^{\mathrm{e}}{ }_{0} \cdot\left(\mathrm{M}^{2}\right) \cdot{ }^{\mathrm{e}}{ }_{1} \ldots\left(\mathrm{M}^{2^{n-2}}\right)_{\mathrm{n}-2}^{\mathrm{e}} \cdot\left(\mathrm{M}^{2^{n-1}}\right)_{\mathrm{e}-1}^{\mathrm{e}}
\end{aligned}
$$

and it corresponds to the algorithm procedure.
For example $\mathrm{M}^{23}=\mathrm{M}^{(10111) \mathrm{b}}=\left(\left(\left(\left((1 . \mathrm{M}) . \mathrm{M}^{2}\right) . \mathrm{M}^{4}\right)\right) \cdot \mathrm{M}^{16}\right)$.
Ignoring multiplication by the initial 1 and also ignoring the last squaring which will not be used, the number of multiplications required is $\left\lfloor\log _{2}{ }^{\mathrm{e}}\right\rfloor+v(\mathrm{n})-1$. Squaring and multiplication can be performed in parallel in this method [Riv84] [Kor95] [Poc98]. Two extra registers are needed to keep the partial result and the squaring result.

